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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/786,370	02/24/2004	Dirk Reese	015114-067500US	3403
26059	7590 03/15/2005		EXAMINER	
TOWNSEND AND TOWNSEND AND CREW LLP/ 015114			ZWEIZIG, JEFFERY SHAWN	
8TH FLOO	ARCADERO CENTER R		ART UNIT	PAPER NUMBER
SAN FRAN	SAN FRANCISCO, CA 94111-3834		2816	
			DATE MAILED: 03/15/2005	5

Please find below and/or attached an Office communication concerning this application or proceeding.

			H.H.
	Application No.	Applicant(s)	
	10/786,370	REESE ET AL.	
Office Action Summary	Examiner	Art Unit	
	Jeffrey S. Zweizig	2816	
The MAILING DATE of this communica Period for Reply	ition appears on the cover sheet wi	th the correspondence address	
A SHORTENED STATUTORY PERIOD FOR THE MAILING DATE OF THIS COMMUNICA - Extensions of time may be available under the provisions of after SIX (6) MONTHS from the mailing date of this communi - If the period for reply specified above is less than thirty (30) of - If NO period for reply is specified above, the maximum statut - Failure to reply within the set or extended period for reply will Any reply received by the Office later than three months after earned patent term adjustment. See 37 CFR 1.704(b).	ATION. 37 CFR 1.136(a). In no event, however, may a recitation. days, a reply within the statutory minimum of thirt fory period will apply and will expire SIX (6) MON I, by statute, cause the application to become AB	eply be timely filed by (30) days will be considered timely. THS from the mailing date of this communic SANDONED (35 U.S.C. § 133).	cation.
Status			
 1) Responsive to communication(s) filed 2a) This action is FINAL. 3) Since this application is in condition for closed in accordance with the practice)⊠ This action is non-final. r allowance except for formal matt	·	ts is
Disposition of Claims			
4) Claim(s) 1-20 is/are pending in the approach 4a) Of the above claim(s) is/are 5) Claim(s) 9-12 is/are allowed. 6) Claim(s) 1,13,14,17 and 18 is/are reject 7) Claim(s) 2-8,15,16,19 and 20 is/are ob 8) Claim(s) are subject to restriction. Application Papers 9) The specification is objected to by the Base 200 to the 2004 is	withdrawn from consideration. cted. bjected to. on and/or election requirement.		
10)⊠ The drawing(s) filed on <u>09 July 2004</u> is Applicant may not request that any objection Replacement drawing sheet(s) including the 11)□ The oath or declaration is objected to be	on to the drawing(s) be held in abeyar ne correction is required if the drawing	nce. See 37 CFR 1.85(a). (s) is objected to. See 37 CFR 1.1	` ,
Priority under 35 U.S.C. § 119	,		
12) Acknowledgment is made of a claim for a) All b) Some * c) None of: 1. Certified copies of the priority do	ocuments have been received. Ocuments have been received in A the priority documents have been al Bureau (PCT Rule 17.2(a)).	pplication No received in this National Stage	3
Attachment(s)			
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTC 3) Information Disclosure Statement(s) (PTO-1449 or PT Paper No(s)/Mail Date)-948) Paper No(s	Summary (PTO-413) s)/Mail Date nformal Patent Application (PTO-152) 	

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Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Voldman (6,404,269).

Fig. 6 discloses a pmos inverter device 105, a pmos stacked device 103, an nmos stacked device 104 and an nmos inverter device 106 as recited in claim 1. The gates receive bias voltages that vary with the supply voltage Vdd/Vss as recited in claim 1.

3. Claims 13, 14, 17 and 18 are rejected under 35 U.S.C. 102(b) as being anticipated by Chan et al. (6,140,846).

Figs. 1 and 2 discloses an input stage 136/138, an output stage 132/134, a bias circuit 150, a first pmos 134, a first device 264 and a second device 256 connected and functioning as recited in claim 13.

The gate of the second device 256 is coupled to the supply VDD2 as recited in claim 14.

Further shown is a second pmos 132 as recited in claim 17.

Further shown is a voltage translator 120 as recited in claim 18.

Conclusion

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4. Claims 2-8, 15, 16, 19, and 20 are objected to as being dependent upon a rejected base claim, but may be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. Claims 9-12 appear to be allowable over the Prior Art of record.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeffrey S. Zweizig whose telephone number is (571) 272-1758. The examiner can normally be reached on Monday thru Thursday 6:00 am to 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan can be reached on (571) 272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jeffle/S. Zweizig Primary Examiner Art Unit 2816